Worksheet 3
Introduction to Microprocessor and Organization of 8085 Microprocessor

Q.1 What is an Interrupt ? Explain in detail.

Q.2 What is Hardware interrupt. Explain all 8085 hardware interrupts with their priorities and vector addresses.

Q.3 What is software interrupt. Explain all 8085 software interrupts with their priorities and vector addresses.

Q.4 Differentiate between hardware and software interrupts of 8085 microprocessor.

Q.5 Differentiate between maskable and non-maskable interrupts.

Q.6 Explain organization of ALU with the help of simple block diagram.

Q.7 Select the correct alternative and rewrite the following:

1. Trap is an interrupt with ____________ priority.
   (i) highest (ii) lowest (iii) intermediate (iv) none of these

2. _____ is not a vectored interrupt.
   (i) Trap (ii) INTR (iii) RST 7.5 (iv) RST 6.5

3. Trap is a __________ interrupt.
   (i) maskable (ii) non-maskable (iii) non-vectored (iv) none of these

4. ___________ is the vector address of Trap
   (i) 0024H (ii) 002CH (iii) 003CH (iv) 0034H

5. ___________ is the vector address of RST7.
   (i) 0000H (ii) 0030H (iii) 0038H (iv) 0018H